Parallel Autotuning Compilation for Algorithmic Modelica on Nvidia 2 teraflop 2050 GPU

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At PELAB, together with the Open Source Modelica Consortium (an international open source effort supported by 38 organizations, see www.openmodelica.org) the OpenModelica environment including the OpenModelica Compiler (OMC) of the Modelica language including MetaModelica extensions is developed. Modelica is a high level language supporting equations and matrix operation. The development is open source.

Currently OMC compiles Modelica/MetaModelica into C-code via several optimizing steps. The development is supported by an Eclipse plug-in MDT (Modelica Development Tooling), also including a debugger, and a template language already used for developing code generators to C and C#. There has earlier been developed several parallel code generator prototypes ([2] – [5]) in the OpenModelica system including generation of OpenCL code for Nvidia. However, static compilation schemes sometimes fail to parallelize due to conservative assumptions of dependencies.

The goal of this master thesis project is to design and implement an efficient parallel autotuning compilation scheme from Modelica to the NVIDIA Fermi GPGPU. The development need not be done from scratch, it can build on and re-use part of 7000 line C++ implementation with good results for the Cell BE described in [1]. The idea is run-time construction of intermediate code for fragments of models/programs with many matrix/array operations. The run-time construction gives exact knowledge of dependencies. The intermediate code is downloaded into the GPU and executed whenever some computation results is needed. The master thesis work includes adapting this approach to the Modelica language, and to Fermi GPGPU instead of the previous Cell BE.

The master thesis project requires knowledge of compiler construction, parallel programming, as well as some experience and interest in advanced programming.

References:

[1] Raymes Khoury, Bernd Burgstaller and Bernhard Scholz. Accelerating the Execution of Matrix Languages on the Cell Broadband Engine Architecture. Accepted to the IEEE Transactions on Parallel and Distributed Systems, 2012. (Send email to Mahder or Olena to get a .pdf copy of the paper)


